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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jeffrey S. Mailloux et al. Examiner: Hong Kim
Serial No.: 08/984,563 Group Art Unit: 2187
Filed: December 3, 1997 Docket: 303.623US4
Title: ASYNCHRONOUSLY-ACCESSIBLE MEMORY DEVICE WITH MODE
SELECTION CIRCUITRY FOR BURST OR PIPELINED OPERATION

PRELIMINARY AMENDMENT

RECEIVED

MAY 20 2002

Technology Center 2100

Box CPA
Commissioner for Patents
Washington, D.C. 20231

Before taking up the above-identified Continuing Prosecution Application for examination, please enter the following amendments and consider the following remarks.

IN THE CLAIMS

Please substitute the claim set in the appendix entitled Clean Version of Pending Claims for the previously pending claim set. The substitute claim set is intended to reflect the amendment of previously pending claims 63 and 83 to correct typographical errors. No new matter has been added. The specific amendments to individual claims are detailed in the following marked up set of claims.

63. (Amended) A method of accessing a memory, comprising:
receiving an external row address;
selecting a burst mode of operation or a pipeline mode of operation of the memory;
selecting a read operation or a write operation for the memory;
selecting an external address only data path, obtaining an external column address, and obtaining information from the memory if the read operation of the pipeline mode of operation is selected;
selecting an external address only data path, obtaining an external column address, and providing information to the memory if the write operation of the pipeline mode of operation is selected;
selecting an initial buffered external address data path, obtaining an initial external